



Docket No.: A7995.0001/P001
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Design Application of:
Virgil V. Wilkins, et al.

Application No.: 09/716,195

Group Art Unit: 2182

Filed: November 17, 2000

Examiner: K. Huynh

For: INTEGRATED INPUT/OUTPUT
CONTROLLER

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INFORMATION DISCLOSURE STATEMENT (IDS)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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JUN 30 2003

Technology Center 2100

Dear Sir:

Pursuant to 37 CFR 1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is filed before the mailing date of a first Office Action on the merits.

While the information and references disclosed in this Information Disclosure Statement may be "material" pursuant to 37 CFR 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 CFR 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56(a) exists. It is submitted that the Information Disclosure Statement is in compliance with 37 CFR 1.98 and the Examiner is respectfully requested to consider the listed references.

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 04-1073, under Order No. A7995.0001/P001.

Dated: June 27, 2003

Respectfully submitted,

By 

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